

07/30/03

16558 U.S. PTO

FORM PTO-1082

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re application of:

Etsuko TERASAWA and Yoshiteru ONO, both of Nagano-ken, Japan

Serial No: Not Assigned

Filed: July 30, 2003

For: Semiconductor Device, Design Method For Semiconductor Device, Design
Apparatus and Design Program For Semiconductor Device

Mail Stop PATENT APPLICATION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

CORRESPONDENCE ADDRESS:

☒ Customer Number

000026021



26021

PATENT TRADEMARK OFFICE

Transmitted herewith for filing is the patent application identified above.

☒ 9 sheet(s) of drawings (☒ formal ☐ informal) is(are) enclosed.☒ 45 page(s) of specification and 1 page(s) of abstract of the invention are enclosed.☒ An assignment of the invention to SEIKO EPSON CORPORATION ☐ is enclosed ☒ will follow.☐ An associate power of attorney ☐ is enclosed ☐ will follow.☐ Applicant claims small entity status. See 37 CFR 1.27.☒ Declaration and Power of Attorney ☐ is enclosed ☒ will follow.☒ A certified copy of Japanese Patent Application No. 2002-221261 filed July 30, 2002 from which priority is claimed under 35 U.S.C. § 119 is enclosed.☒ IDS enclosed (☒ with 5 reference(s)).☐ Preliminary Amendment is enclosed.☐ Application data sheet is enclosed. See 37 CFR 1.76.☒ Return postcard is enclosed.

| CALCULATION OF FEES | | | | | | | | | |
|---------------------|--|---------------------|-----|-------------------------|--|------|-----------|-----------|-------|
| ITEM | | TOTAL NO. OF CLAIMS | | NO. OF CLAIMS OVER BASE | LG/SM \$ ENTITY FEE | | \$ AMOUNT | \$ FEE | |
| A | TOTAL CLAIMS FE E | 25 | -20 | 5 | LG=\$18 SM=\$9 | \$18 | 90 | | |
| B | INDEPENDENT CLAIMS FEE* | 3 | -3 | 0 | LG=\$84 SM=\$42 | \$84 | 0 | | |
| C | SUBTOTAL - ADDITIONAL CLAIMS FEE (ADD FINAL COLUMN IN LINES A + B) | | | | | | | 90 | |
| D | MULTIPLE-DEPENDENT CLAIMS FEE | | | | LARGE ENTITY FEE = \$280 SMALL ENTITY FEE = \$140 | | | \$ 0 | |
| E | BASIC FEE | | | | LARGE ENTITY FEE = \$750 SMALL ENTITY FEE = \$375 | | | \$ 750.00 | |
| F | TOTAL FILING FEE (ADD TOTALS FOR LINES C, D, AND E) | | | | | | | \$ 840.00 | |
| G | ASSIGNMENT RECORDING FEE | | | | | | | \$ 40 | \$ 40 |
| | *LIST INDEPENDENT CLAIMS 1, 10, 18 | | | | | | | | |

☐ Please charge my Deposit Account No. 50-1314 the amount of \$ 0. A copy of this letter is enclosed.☐ A check in the amount of \$ 0 to cover the filing fee is enclosed.☐ A check in the amount of \$ 40.00 to cover Assignment Recordation fee is enclosed.☐ The Commissioner is hereby authorized to charge any deficiency for the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-1314. A copy of this sheet is enclosed.☐ Any additional filing fees required under 37 C.F.R. 1.16☐ Any patent application processing fees under 37 C.F.R. 1.17Respectfully submitted
HOGAN & HARRISON LLP

By:

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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TERASAWA, et al.

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For: Semiconductor Device, Design Method for
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Program for Semiconductor Device

Art Unit: Not Assigned

Examiner: Not Assigned

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Date of Deposit: July 30, 2003

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P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

I hereby certify that

- ☒ two copies of a letter of transmittal
- ☒ patent application (45 page(s) of specification; 25 claim(s); 1 page(s) of abstract
- ☒ 9 sheet(s) of formal drawings
- ☒ certified copy of Japanese patent application No. 2002-221261 which was filed July 30, 2002 from which priority is claimed in the subject case pursuant to 35 U.S.C. § 119
- ☒ Information Disclosure Statement with 5 references
- ☒ return postcard

are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service with sufficient postage under 37 C.F.R. § 1.10 on the date indicated above and are addressed to:

Mail Stop PATENT APPLICATION

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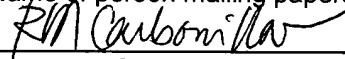
Arlington, VA 22313-1450

Date: July 30, 2003

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